IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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First Named Inventor: Bulucea, Constantin

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Examiner:

Farahani, D.

Atty. Docket No.:

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(P04971)

Title:

Gate-Enhanced Junction Varactor

Assignee:

National Semiconductor Corporation

Santa Clara, California

COMMISSIONER FOR PATENTS PO Box 1450 Alexandria, Virginia 22313-1450

POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST, CORRESPONDENCE ADDRESS INDICATION, AND ACCOMPANYING ASSIGNEE CERTIFICATION UNDER 37 CFR 3.73(b)

Sir:

The above-identified assignee, National Semiconductor Corporation, a Delaware corporation, hereby revokes all powers of attorney previously given in the above-identified patent application and hereby appoints the practitioner(s) associated with Customer Number:

88308

as attorney(s) and/or agent(s) to prosecute the above-identified application and to transact all business in the U.S. Patent and Trademark Office in connection therewith.

The above-identified assignee further requests that the U.S. Patent and Trademark Office recognize or change the correspondence address for the above-identified application to the address associated with the above-mentioned Customer Number.

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ASSIGNEE CERTIFICATION UNDER 37 CFR 3.73(b)

The undersigned representative of the above-identified assignee certifies that the above-identified assignee is the assignee of the entire right, title, and interest in the above-identified patent application by virtue of the following chain of title from the inventor(s) of the above-identified application to the above-identified assignee: An assignment from inventor Constantin Bulucea to National Semiconductor Corporation on 6 July 2001, the assignment having been recorded in the U.S. Patent and Trademark Office at Reel 012010, Frame 0308.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified assignee.

Signature:

Date: 21 Sep 7011

Name:

Andrew S. Viger

Title:

IP Counsel

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